

14 pin Dual-in-Line

Frequency range 50.01MHz to 200MHz

GV14 VCXOs, are packaged in an industry-standard, 14 pin Dual in Line package. The VCXO incorporates a high Q fundamental mode

50.01MHz to 200.0MHz

2.3ps typical, 4.0ps maximum

4.0ps typical (for 155.250MHz)

Tune to the nominal frequency

From ±30ppm to ±150ppm

10ms maximum, 5ms typical

25mA maximum (15pF load) 10% maximum, 6% typical

Monotonic and Positive. (An

increase of control voltage always increases output

±5ppm per year maximum

Not available (4 pad package)

1.2ns typical (15pF load)

25kHz minimum

2 M Ω minimum

-50° to +100°C

Fully compliant

frequency.)

50% ±5% measured at 50% Vdd

with Vc= 1.65 ±0.2VDC

90% Vdd minimum

10% Vdd maximum

27.0ps typical (for 155.250MHz)

3.3 VDC ±5%

(for 155.250MHz)

See table below

LVCMOS

See table

15pF

- LVCMOS Output
- Supply Voltage 3.3 VDC
- High Q fundamental mode crystal

crystal and a low jitter multiplier circuit.

- Low jitter multiplier circuit
- Low unit cost

DESCRIPTION

SPECIFICATION

Frequency Range:

Period Jitter RMS:

Phase Noise:

Pulling Range:

Output Load:

Duty Cycle:

Linearity:

Ageing:

RoHS Status:

Start-up Time:

Rise/Fall Times:

Slope Polarity:

(Transfer function)

Storage Temperature:

Enable/Disable (Tristate):

Integrated Phase Jitter:

Period Jitter Peak to peak:

Initial Frequency Accuracy:

Output Voltage HIGH (1):

Output Voltage LOW (0):

Temperature Stability:

Current Consumption:

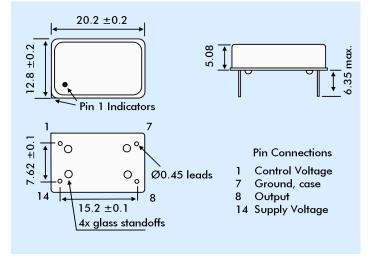
Modulation Bandwidth: Input Impedance:

Supply Voltage: Output Logic:





OUTLINE & DIMENSIONS



PHASE NOISE

Offset	Frequency 155.25MHz
10Hz	-65dBc/Hz
100Hz	-95dBc/Hz
1kHz	-120dBc/Hz
10kHz	-128dBc/Hz
100kHz	-122dBc/Hz
1MHz	-120dBc/Hz
10MHz	-140dBc/Hz

FREQUENCY STABILITY

Stability Code	Stability ±ppm	Temp. Range
А	25	0°~+70°C
В	50	0°~+70°C
С	100	0°~+70°C
D	25	-40°~+85°C
E	50	-40°~+85°C
F	100	-40°~+85°C
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If non-standard frequency stability is required Use 'l' followed by stability, i.e. I20 for ±20ppm

PART NUMBERING

